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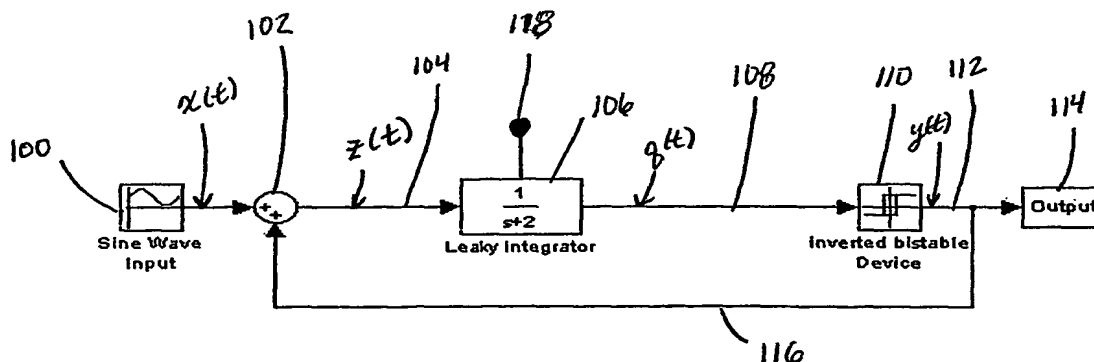
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(54) Title: BINARY DELTA-SIGMA MODULATOR



(57) Abstract: A modulator, including an integrator (118), a bistable device (110), an adder (102) and a feedback loop (116) is provided. A method for converting a continuous time signal to a binary signal is also provided.